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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/673,822	09/29/2003	Peter Hazucha	80107.040US1	2204
7590 06/10/2005 LeMoine Patent Services, PLLC c/o PortfollioIP P.O.Box 52050 Minneapolis, MN 55402			EXAMINER VU, BAO Q	
			ART UNIT 2838	PAPER NUMBER

DATE MAILED: 06/10/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/673,822

Applicant(s)

HAZUCHA ET AL.

Examiner

Bao Q. Vu

Art Unit

2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 March 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-10, 14-23 and 25-30 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-10, 14-23, 25-30 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

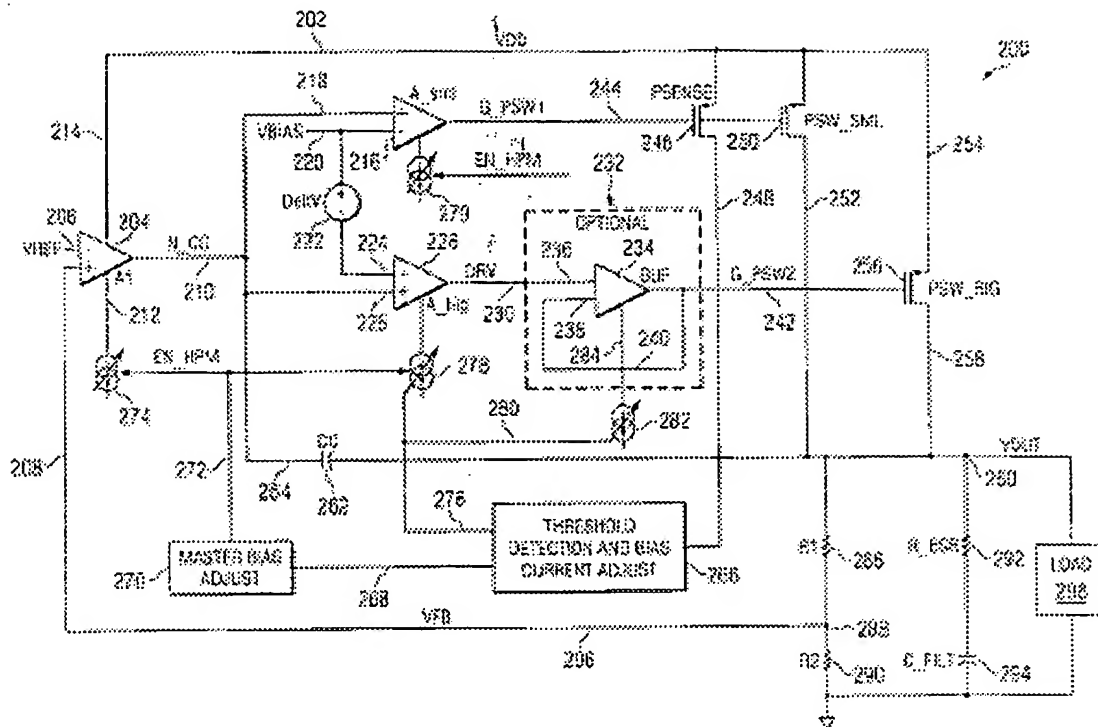
Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-7, 22-24 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Xi (USP 6,677,735). Xi discloses a sleep transistor or regulator transistor (240), a load (298), a power supply node (202), a control circuit (204, 216, 226) to influence the operation of the sleep or pass regulator transistor with first control loop (266), and a second loop (208).



Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 8, 9, 26-29 and 30 are rejected under 35 U.S.C. 103(a) as being unpatentable over Xi (USP 6,677,735) in view of Park (USP 6,775,784) Xi teaches a sleep transistor or regulator transistor with a plurality of error amplifier circuits (See above). Park teaches sleep transistors utilizing a memory circuit and memory cell arrays. (See figure 2.) It would have been obvious to one having ordinary skill in the art

at the time of the invention was made to provide the a memory circuit and memory cell arrays in the field of regulator circuits utilizing sleep transistors of Keshavarzi with the sleep transistors with a plurality of error amplifiers utilizing a memory circuit and memory cell arrays of Park, in order to provide advanced power management.

5. Claims 10-21 and 28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Xi (USP 6,677,735) in view of Park (USP 6,775,784 and further in view of Keshavarzi et al. (USP 6,765,414). Xi teaches a sleep transistor or regulator transistor with a plurality of error amplifier circuits (See above); Park teaches sleep transistors utilizing a memory circuit and memory cell arrays. (See figure 2.) It would have been obvious to one having ordinary skill in the art at the time of the invention was made to provide the use of multiplexers in field regulator circuits utilizing sleep transistors of Keshavarzi with the sleep transistors with a plurality of error amplifiers utilizing a memory circuit and memory cell arrays of Xi and Park, in order to exert a digital control signal for the precise control of the sleep transistor.

Response to Arguments

6. Applicant's arguments with respect to the claims have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Bao Q. Vu whose telephone number is (571) 272-2088. The examiner can normally be reached on Monday-Fridays, 8:00AM- 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael S. Sherry can be reached on (571) 272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Bao Q. Vu
Primary Examiner
Art Unit 2838

June 7, 2005